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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Nigel Peter Topham)

Serial No. 09/903,208)

Filed: July 11, 2001)

For: INSTRUCTION SETS FOR
PROCESSORS)

Art Unit: 2183)

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this date.

29 March 02 *RFB*
Date
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Registration No. 29367

Appr. February 20, 1998

Attorney for Applicant

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

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Sir:

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant through counsel herewith submits copies of the publications as set forth in the attached form PTO-1449 as follows:

FOREIGN PATENT DOCUMENTS

<u>DOCUMENT NO.</u>	<u>COUNTRY</u>	<u>PUBLICATION DATE</u>
813 144	Europe	Dec. 17, 1997

OTHER DOCUMENTS

1. "OPCODE remap and Compression in Hard-wired Risc Microprocesor;"
IBM Technical Disclosure Bulletin; Vol. 32, No. 10A; page 349; March 1990.

OTHER DOCUMENTS CONT'D.

2. "A Programmable Instruction Format Extension to VLIW Architectures;"
De Gloria et al.; IEEE; pages 35-40; March 1992.

REMARKS

The above-listed references were cited in a European Search Report dated February 7, 2002 (copy of search report and references enclosed).

Applicant respectfully requests that the Examiner consider the above-listed references in the examination of this application and list these references of record in the application.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.



By

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March 29, 2002

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Sheet 1 of 1

Form PTO-1449 U.S. Department of Commerce (Rev. 8-88) Patent and Trademark Office INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	Attorney Docket No.: 0808.65688	Serial No.: 09/903,208
	Applicant: Topham	
	Filing Date: July 11, 2001	Group: 2183

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	813 144	Dec. 17, 1997	Europe			X	

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	"OPCODE remap and Compression in Hard-wired Risc Microprocesor;" IBM Technical Disclosure Bulletin; Vol. 32, No. 10A; page 349; March 1990.
	"A Programmable Instruction Format Extension to VLIW Architectures;" De Gloria et al.; IEEE; pages 35-40; March 1992.

Examiner	Date Considered
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.